



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/789,903

Filed: February 27, 2004

Inventor(s):

Scott C. McLeod

Aniruddha Bashar

Title: HIGHLY ACCURATE  
SWITCHED CAPACITOR  
DAC

§ Examiner: Unknown  
§ Group/Art Unit: 2819  
§ Atty. Dkt. No: 5707-04000  
§  
§  
§  
§  
§  
§  
§  
§  
§  
§

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

Jeffrey C. Hood

Signature

Date

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☐ the additional information identified below in paragraph 3. A copy of each reference listed on the Form PTO-1449 is enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);  
☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;  
☐ before the mailing date of a first Office Action on the merits; or  
☒ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.

- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed below.
- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed below.

2. It is hereby certified:

- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or
- ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. ☐ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

4. For each non-English language reference listed on the attached Form PTO-1449:

- ☐ reference is made to an English language translation submitted herewith, and/or
- ☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
- ☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- ☐ reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or
- ☐ reference is made to the concise explanation set forth below:

5. ☐ Applicant also offers the following comments for the Examiner's consideration:

6. ☐ Also enclosed is a copy of a foreign search report citing these references.

7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.

8. ☐ Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

Applicant does not believe that any fees are required relating to this Information Disclosure Statement. The Commissioner is hereby authorized to charge any fees which may be required or credit any overpayment to Meyertons, Hood, Kivlin, Kowert & Goetzel PC, Deposit Account No. 50-1505/5707-04000/JCH.

Respectfully submitted,



Jeffrey C. Hood  
Reg. No. 35,198  
Attorney for Applicant(s)

MEYERTONS HOOD KIVLIN KOWERT & GOETZEL PC  
P. O. Box 398  
Austin, Texas 78767  
(512) 853-8800

Date: 11/30/2024

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5707-04000		SERIAL NO: 10/789,903			
APPLICANT: McLeod et al.							
FILING DATE: February 27, 2004				GROUP: 2819			
<b>U.S. PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	A1	Fredriksen, Einar; Tormod Njolstad, and Runar Sorasen, "Evaluation of algorithms for dynamic element matching in a Delta-Sigma A/D converter," undated, one page.					
	A2	Andersson, Niklas U., "A Comparison of Dynamic Element Matching in Digital-to-Analog Converters," undated, 17 pages.					
	A3	Andersson, Niklas U.; K. Ola Andersson, Mark Vesterbacka, and J. Jacob Wikner, "Models and Implementation of a Dynamic Element Matching DAC," Analog Integrated Circuits and Signal Processing, January 2003, Vol. 34, No. 1, pp. 5-6(2).					
	A4	Tuthill, Mike, "A Switched-Current, Switched-Capacitor Temperature Sensor in 0.6-μm CMOS," IEEE Journal of Solid State Circuits, Vol. 33, No. 7, July 1998, pp.1117-1122.					
	A5	Rombouts, P.; L. Weyten, J. Raman, and S. Audenaert, "Capacitor Mismatch Compensation for the Quasi-Passive Switched-Capacitor DAC," IEEE Transactions on Circuits and Systems-I: Fundamental Theory and Applications, Vol. 45, No. 1, January 1998, pp.68-71.					
EXAMINER:				DATE CONSIDERED:			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.							